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REMARKS

The Applicants request reconsideration of the rejection.
Claims 1-29 are now pending.

Claim 13 was rejected under 35 U.S.C. §101 as being directed to non-statutory subject matter. The Applicants have amended claim 13 to adopt the Examiner's helpful suggestion on Page 2 of the Office Action, to overcome this rejection.

Claim 19 was rejected under 35 U.S.C. §112, second paragraph, as containing insufficient antecedent basis. Claim 19 has been amended to address the Examiner's concern. Claims 1-4, 13, 16, 19-21, and 24 were rejected under 35 U.S.C. §102(b) as being anticipated by Mattson et al., U.S. 4,463,424 (Mattson). The Applicants traverse as follows.

Two features present in original Claim 1, which are not taught by Mattson, are the means for acquiring information about process details to be executed by the database management system, and the means for acquiring information about data mapping from the computer and the storage device. In accordance with these two items of information, the claimed means for creating planned cache space settings performs its function using the acquired information.

Although Claim 1 has been amended to strengthen the meaning of "process details" and "acquired information" used

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for creating the planned cache space settings, Claim 1 has not been narrowed by these amendments. Further, in referring to the passages noted by the Examiner in Columns 5 and 7 of the patent, the Applicants respectfully submit that Mattson does not teach that information about process details or about data mapping are acquired in accordance with the claim.

Note specifically that Column 7, lines 15-22 disclose that requests to data stored on DSD are referenced to a physical location, whether requests for reading, writing, or erasing data. Further, Column 5, lines 25-35 describe a flow of the cache function in which CPU accesses the storage subsystem by way of generating read and write requests with CCW reference to storage. If the data has been copied to cache previously, then the request satisfied with respect to the cache. If not, the reference is passed to DASD. Then, the reference is staged-up as is well known. Mattson uses a least recently used (LRU) destaging algorithm.

However, Mattson does not disclose that any information is acquired about database management processing details. Further, Mattson does not disclose that information about data mapping is acquired from the computer and the storage device. Moreover, Mattson does not disclose that these types of

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information are acquired by means forming part of a device for setting cache space as required by Claim 1.

Turning to independent Claim 13, which is directed to a computer-executable program, similar limitations are found.

Independent Claim 16, however, is directed to a method for managing a data cache which does not expressly recite the steps discussed above. However, Claim 16 is limited by requiring that a cache effect function be specified for each data structure, and that the cache effect function be specified and space in the data cache allocated to each data structure using the cache effect function. While Mattson does disclose sorting of data structures in various caches, the cache effect function is previously determined and is not specified for each data structure. Further, the cache effect function or space in the data cache allocated to each data structure, using the cache effect function as required by Claim 16.

Each of Claims 2-4, 19-21, and 24 is dependent on one of these claims and thus is patentable at least based upon the subject matter discussed above.

New Claims 25-29 combine various features of otherwise-indicated allowable subject matter with the base subject

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matter of Claim 1. Therefore, these claims are also patentable.

In view of the foregoing amendments and remarks, the Applicants request reconsideration of the rejection and allowance of the claims.

Respectfully submitted,



Daniel J. Stanger
Registration No. 32,846
Attorney for Applicants

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314
Telephone: (703) 684-1120
Facsimile: (703) 684-1157
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